AMENDMENTS TO THE CLAIMS

 (Currently Amended) Growth method of nitride semiconductor epitaxial layer comprising:

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- a first step of growing a second nitride semiconductor epitaxial layer on a first nitride semiconductor epitaxial layer at a first temperature;
- a second step of growing a third nitride semiconductor epitaxial layer on the second nitride semiconductor epitaxial layer at a second temperature; and
- a third step of releasing nitrogen from the second nitride semiconductor epitaxial layer by <u>collectively</u> increasing a temperature <u>of the first nitride semiconductor epitaxial layer</u>, the second nitride semiconductor epitaxial layer, and third nitride semiconductor epitaxial layer to a third temperature higher than the second temperature.
- (Original) The growth method of nitride semiconductor epitaxial layer of claim 1, wherein the first and third nitride semiconductor epitaxial layers are made of a material whose equilibrium vapor pressure of nitrogen is lower than that of the second nitride semiconductor epitaxial layer.
- (Original) The growth method of nitride semiconductor epitaxial layer of claim 1, the second nitride semiconductor epitaxial layer is converted into a metal layer in the third step.
- 4. (Original) The growth method of nitride semiconductor epitaxial layer of claim 1, further comprising:
 - a fourth step of growing a fourth nitride semiconductor epitaxial layer on the third nitride semiconductor epitaxial layer after releasing nitrogen from the second nitride semiconductor epitaxial layer.
- 5. (Original) The growth method of nitride semiconductor epitaxial layer of claim 1, wherein the second nitride semiconductor epitaxial layer is made of $In_xGa_{1-x}N$ (0.5 < x \leq 1).

6 (Original) The growth method of nitride semiconductor epitaxial layer of claim 1. wherein the first and third nitride semiconductor epitaxial layers are made of $Al_vGa_{1,v}N$ (0 < x < 1).

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- 7. (Original) The growth method of nitride semiconductor epitaxial layer of claim 1, wherein the first temperature in the first step is in a range of 300°C to 800°C.
- 8. (Original) The growth method of nitride semiconductor epitaxial layer of claim 1, wherein the second temperature in the second step is in a range of 300°C to 800°C.
- 9. (Original) The growth method of nitride semiconductor epitaxial layer of claim 1. wherein the third nitride semiconductor epitaxial layer has a thickness in a range of 1 nm to 100nm.
- 10. (Original) The growth method of nitride semiconductor epitaxial layer of claim 1, wherein the third temperature in the third step is 900°C or more.
- 11. (Original) The growth method of nitride semiconductor epitaxial layer of claim 1. wherein the first nitride semiconductor epitaxial layer is grown on a substrate.
- 12. (Original) The growth method of nitride semiconductor epitaxial layer of claim 11, wherein the first nitride semiconductor epitaxial layer comprises a buffer layer grown at a relatively low temperature and an un-doped GaN layer grown on the buffer layer.
- 13. (Original) The growth method of nitride semiconductor epitaxial layer of claim 1. further comprising:
 - a step of patterning the third nitride semiconductor epitaxial layer, prior to the third step.
- 14. (Original) The growth method of nitride semiconductor epitaxial layer of claim 3. further comprising:
 - a step of separating a part including the first nitride semiconductor epitaxial layer from the other part including the third nitride semiconductor epitaxial layer.

15. (Currently Amended) Growth method of nitride semiconductor epitaxial layer comprising: growing a buffer layer on a substrate and an un-doped GaN layer on the buffer layer; growing InN layer on the un-doped GaN layer:

growing a GaN layer on the InN layer;

converting the InN layer into a metal layer by collectively increasing a temperature of the buffer layer, the un-doped GaN layer, the InN layer, and the GaN layer; and growing $Al_x[[L]]ln_yGa_{1-y}N$ ($0 \le x \le l, 0 \le y \le l$) on the GaN layer.

- (Currently Amended) Growth method of nitride semiconductor epitaxial layer comprising:
 - a first step of growing a first nitride semiconductor epitaxial layer containing indium at a first temperature;
 - a second step of growing a second nitride semiconductor epitaxial layer whose equilibrium vapor pressure of nitrogen is <u>higher lower</u> than that of the first nitride semiconductor epitaxial layer, on the first nitride semiconductor epitaxial layer at a second temperature; and
 - a third step of releasing nitrogen from the first nitride semiconductor epitaxial layer by <u>collectively</u> increasing a temperature <u>of the first nitride semiconductor epitaxial layer</u>, the <u>second nitride semiconductor epitaxial layer</u>, and third nitride <u>semiconductor epitaxial layer</u> to a third temperature <u>lowerhigher</u> than the second temperature to convert the first nitride semiconductor epitaxial layer into a metal layer.
- (Original) The growth method of nitride semiconductor epitaxial layer of claim 16, wherein the first nitride semiconductor epitaxial layer is grown on a substrate.
- 18. (Original) The growth method of nitride semiconductor epitaxial layer of claim 16, wherein the first nitride semiconductor epitaxial layer is grown on a compound semiconductor epitaxial layer grown on a substrate.

nitride semiconductor epitaxial layer is made of GaN.

19. (Original) The growth method of nitride semiconductor epitaxial layer of claim 17, the first nitride semiconductor epitaxial layer is made of $In_xGa_{1-x}N$ (0.5 < $x \le 1$) and the second

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